



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cook et al.

Serial No.: 09/552,563

Filed: March 10, 2000

For: CONTROL SYSTEMS AND  
METHODS

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: Art Unit: 2858  
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: Examiner: Patel, P.  
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**AMENDMENT**

Commissioner for Patents  
Box DAC  
Washington, D.C. 20231

Sir:

In response to the Office Action dated July 30, 2002, please amend the above-identified patent application as follows:

IN THE CLAIMS

1. (twice amended) A method for detecting faults in a transducer including a secondary winding having at least two voltage outputs, the transducer being electrically connected to a logic circuit, said method comprising the steps of:

summing the voltage outputs to obtain a summed voltage value;

determining a difference between a current value of the summed voltage value to a reference value; [and]

if an absolute value of the difference between a current value of the summed voltage value and the reference value exceeds a freeze threshold, then maintaining the reference value constant; and

detecting faults in at least one of a rotary voltage differential transducer (RVDT) and a linear voltage differential transducer (LVDT) based on the determined absolute value.

**RECEIVED**

MAR - 5 2003

**OFFICE OF PETITIONS**